

	L #	Hits	Search Text	DBs
1	L1	706	(361/782,783).ccls.	USP AT; US-P GPU B
2	L2	262	1 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
3	L3	75	2 and (overlap\$4 bridg\$3 span\$4)	USP AT; US-P GPU B
4	L4	187	2 not 3	USP AT; US-P GPU B
5	L5	13	4 and (dicing diced singulat\$4)	USP AT; US-P GPU B
6	L6	174	4 not 5	USP AT; US-P GPU B
7	L7	444	1 not 2	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
8	L8	81	7 and (overlap\$4 bridg\$3 span\$4)	USP AT; US-P GPU B
9	L9	20	7 and (dicing diced singulat\$4)	USP AT; US-P GPU B
10	L10	93	8 9	USP AT; US-P GPU B
11	L11	351	7 not 10	USP AT; US-P GPU B
12	L12	4	11 and (street\$1 score\$1)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
1	L1	2361	(257/723,724,777).ccls.	USP AT; US-P GPU B
2	L2	2593	(438/33,67,68,107,110,113,114,460,461,462,463,464,465).ccls.	USP AT; US-P GPU B
3	L3	0	438/982.ccls	USP AT; US-P GPU B
4	L4	4851	1 2	USP AT; US-P GPU B
5	L5	538	4 and @pd>=20021201	USP AT; US-P GPU B
6	L6	109	5 and (overlap\$4 bridg\$3 span\$4)	USP AT; US-P GPU B
7	L7	429	5 not 6	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
8	L8	73	7 and singulat\$4	USP AT; US-P GPU B
9	L9	356	7 not 8	USP AT; US-P GPU B
10	L10	105	9 and dicing	USP AT; US-P GPU B
11	L11	251	9 not 10	USP AT; US-P GPU B
12	L12	60	11 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
13	L13	191	11 not 12	USP AT; US-P GPU B
14	L14	406	438/109.ccls.	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
15	L15	367	14 not 5	USP AT; US-P GPU B
16	L16	81	15 and (dicing singulat\$4)	USP AT; US-P GPU B
17	L17	286	15 not 16	USP AT; US-P GPU B
18	L18	4	17 and diced	USP AT; US-P GPU B
19	L19	282	17 not 18	USP AT; US-P GPU B
20	L20	42	19 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
21	L21	240	19 not 20	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
22	L22	47	21 and (overlap\$4 bridg\$3 span\$4)	USP AT; US-P GPU B
23	L23	193	21 not 22	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
1	L1	21	438/982.ccls.	USP AT; US-P GPU B